

Atty. Docket No. YOR9-2001-0342US1
(590.070)

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A demodulator comprising:

an input arrangement which accepts an input signal;

an active balun circuit which amplifies the input signal and converts the input signal to differential form;

at least one mixer;

said active balun circuit being adapted to drive said at least one mixer;

said active balun circuit comprising a an asymmetrical negative shunt feedback arrangement.

2. (Currently Amended) The demodulator according to Claim 1, wherein said asymmetrical negative shunt feedback arrangement is adapted to apply variable gain in said at least one mixer.

3. (Currently Amended) The demodulator according to Claim 1, wherein said asymmetrical negative shunt feedback arrangement is adapted to provide stabilized input impedance.

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4. **(Original)** The demodulator according to Claim 1, wherein said at least one mixer comprises two mixers, said two mixers being adapted to provide differential outputs.

5. **(Currently Amended)** The demodulator according to Claim 1, wherein said asymmetrical negative shunt feedback arrangement comprises a resistor and a capacitor in series.

6. **(Currently Amended)** The demodulator according to Claim 1, wherein said active balun circuit further comprises:

a transistor;

wherein said asymmetrical negative shunt feedback arrangement connects between the collector and base of said transistor.

7. **(Currently Amended)** The demodulator according to Claim 1, wherein said active balun circuit further comprises:

an inverting output;

wherein said asymmetrical negative shunt feedback arrangement interconnects said input arrangement with said inverting output.

8. **(Currently Amended)** The demodulator according to Claim 7, wherein said active balun circuit further comprises:

a transistor;

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wherein said asymmetrical negative shunt feedback arrangement connects between the collector and base of said transistor in interconnecting said input arrangement with said inverting output.

9. (Original) The demodulator according to Claim 7, wherein said active balun circuit further comprises:

a non-inverting output; and

a compensating arrangement adapted to ensure approximately equal loading on said inverting and non-inverting outputs.

10. (Original) The demodulator according to Claim 9, wherein said compensating arrangement comprises an additional resistor and an additional capacitor in series, said additional resistor and additional capacitor in series being connected between said non-inverting input and ground.

11. (Original) The demodulator according to Claim 7, wherein said active balun circuit further comprises:

a non-inverting output;

said negative shunt feedback arrangement being adapted to reduce the amplitude of said inverting output more than a reduction in the amplitude of said non-inverting output.

12. (Original) The demodulator according to Claim 1, wherein said active balun circuit further comprises:

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an inverting output;

a non-inverting output; and

a first load tuning arrangement connected between said inverting output and power supply voltage; and

a second load tuning arrangement connected between said non-inverting output and power supply voltage.

13. **(Original)** The demodulator according to Claim 1, wherein said active balun circuit further comprises:

an inverting output;

a non-inverting output; and

a first load tuning arrangement connected between said inverting output and power supply voltage; and

a second load tuning arrangement connected between said non-inverting output and ground.

14. **(Currently Amended)** The demodulator according to Claim 1, whereby overall distortion is reduced via said asymmetrical negative shunt feedback arrangement.

15. **(Original)** The demodulator according to Claim 1, wherein said demodulator is adapted for incorporation into a radio-frequency front end of a cellular telephone handset.